



SCHEMATIC DIAGRAM

NOTE
Δ VOLTS AT TRANSMIT CONDITION
ALL VOLTAGES MEASURED FROM PC BOARD GND WITH DC VTM AT NO SIGNAL (AT 13.8 V POWER SUPPLY) IF MEASUREMENT VALUES OBTAINED ARE IN EXCESS OF 200% OF VALUES SHOWN THEN REASON FOR DIFFERENCE SHOULD BE CORRECTED
* CHASSIS GND * PC BOARD GND
‡ VARIABLE

- TRANSISTOR BASE INFORMATION
- TOP VIEW
 - BOTTOM VIEW
 - IC1
 - IC2
 - IC3
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 - IC10
 - IC11
 - IC12
- REAR PANEL
- ANT
 - PA
 - PA
 - EXT SP
 - SP
 - BU
 - J4
 - DC
- FRONT PANEL
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