| | Both RX & TX "A" | Both RX & TX "B" | Both RX & TX "C" | _ | | Both RX & TX "A" | Both RX & TX "B" | Both RX & TX "C" |
|----------------|------------------------|------------------------|------------------------|-----|----------------|------------------------|------------------------|------------------------|
| Ch. 1 (26.965) | 8.1590 | 11.0035 | 7.8025 | | Ch.13 (27.115) | 8.3090 | 11.0035 | 7.8025 |
| Ch. 2 (26.975) | " | 11.0135 | " | | Ch.14 (27.125) | " | 11.0135 | " |
| Ch. 3 (26.985) | " | 11.0235 | " | 1 [| Ch.15 (27.135) | " | 11.0235 | " |
| Ch. 4 (27.005) | " | 11.0435 | " | | Ch.16 (27.155) | " | 11.0435 | " |
| | | | | - | | | | |
| Ch. 5 (27.015) | 8.2090 | 11.0035 | 7.8025 | | Ch.17 (27.165) | 8.3590 | 11.0035 | 7.8025 |
| Ch. 6 (27.025) | " | 11.0135 | " | | Ch.18 (27.175) | " | 11.0135 | " |
| Ch. 7 (27.035) | " | 11.0235 | " | | Ch.19 (27.185) | " | 11.0235 | " |
| Ch. 8 (27.055) | " | 11.0435 | " | | Ch.20 (27.205) | " | 11.0435 | " |
| | | | | - | | | | |
| Ch. 9 (27.065) | 8.2590 | 11.0035 | 7.8025 | | Ch.21 (27.215) | 8.4090 | 11.0035 | 7.8025 |
| Ch.10 (27.075) | " | 11.0135 | " | | Ch.22 (27.225) | " | 11.0135 | " |
| Ch.11 (27.085) | | 11.0235 | | | Ch.23 (27.255) | " | 11.0435 | " |
| Ch.12 (27.105) | = | 11.0435 | | | | | | |

Synthesis: ["A" + "B" + 7.8025 MHz] = on-channel carrier frequency (plus USB and LSB offsets)

Example: For Ch.1, 8.1590 MHz + 11.0035 MHz + 7.8025 MHz = 26.965 MHz. The offsets for LSB and USB are accomplished by totally separate mixing paths in this chassis. Separate synthesizer outputs of 19 MHz for AM/LSB and 34 MHz for USB are used. This is a great improvement in unwanted sideband suppression and image rejection over that of a single synthesizer output stage. Made in the good old days when the cost of a few extra parts wasn't so critical! The RX is single-conversion though, with a 7.8 MHz IF.